

G2113

CMOS Positive Voltage Regulator

Description

The G2113 series of positive, linear regulators feature low quiescent current (30 μ A typ.) with low dropout voltage, making them ideal for battery applications.

These rugged devices have both Thermal Shutdown, and Current Fold-back to prevent device failure under the "Worst" of operating conditions.

The G2113 is stable with an output capacitance of 2.2 μ F or greater.

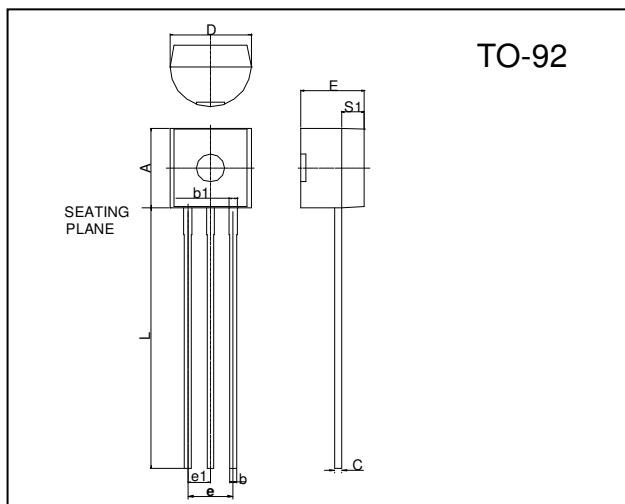
Features

- Very Low Dropout Voltage
- Guaranteed 300mA output
- Over-Temperature Shutdown
- Current Limiting
- Short Circuit Current Fold-back
- Factory Pre-set Output Voltage
- Highly Accurate $\pm 1.5\%$
- Low Temperature Coefficient

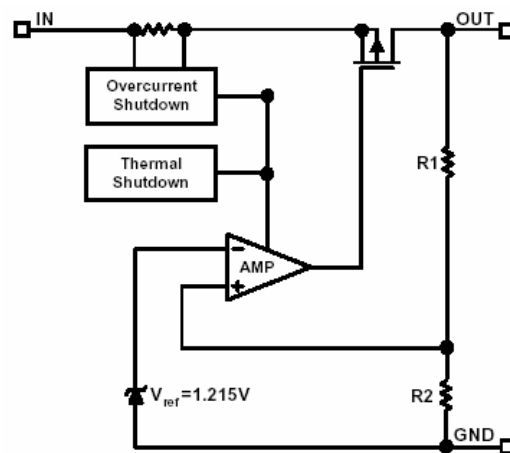
Applications

- Battery Powered Widgets
- Instrumentation
- Wireless Devices
- Cordless Phones
- PC Peripherals
- Portable Electronics
- Electronic Scales

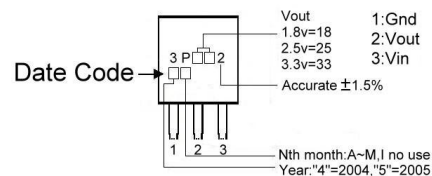
Package Dimensions



Block Diagram

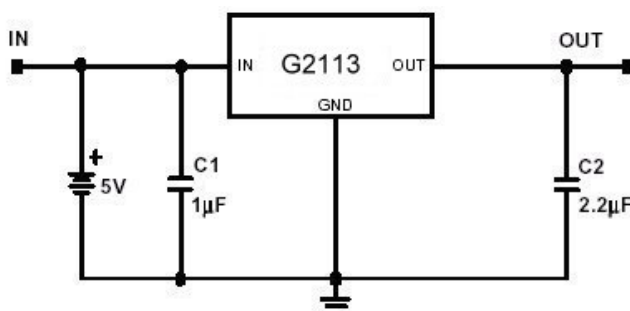


Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.45	4.7	D	4.44	4.7
S1	1.02	-	E	3.30	3.81
b	0.36	0.51	L	12.70	-
b1	0.36	0.76	e1	1.150	1.390
C	0.36	0.51	e	2.42	2.66

Typical Application Circuit



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Voltage	V _{IN}	8	V
Output Current	I _{OUT}	PD/(V _{IN} -V _O)	mA
Output Voltage	V _{OUT}	1.3~3.8	V
Operating Ambient Temperature	T _{opr}	-40 ~ +85	°C
Junction Temperature	T _j	-40 ~ +125	°C
Maximum Junction Temperature	T _{j Max}	150	°C
Thermal Resistance	θ _{jc}	80	°C/W
	θ _{ja}	180	°C/W
Power Dissipation(ΔT=100°C)	PD	625	mW
EDS Classification		B	

Electrical Characteristics Ta=25°C

Parameter	Symbol	Condition		Min	TYP	Max	Unit
Output Voltage	V _{OUT} (E) (Note1)	V _{IN} =V _{OUT} (T)+1V, I _O =1mA		-1.5%	V _{OUT} (T) (Note2)	1.5%	V
Output Current	I _O	V _{IN} =V _{OUT} (T)+2V, V _{OUT} ≥ V _{OUT} (E)*0.96		300	-	-	mA
Current Limit	I _{LIM}	V _O >1.2V		300	450	-	mA
Load Regulation	REG _{LOAD}	V _{IN} =V _{OUT} (T)+2V, I _O =1mA to 300mA		-1	0.2	1	%
Dropout Voltage	V _{DROPOUT}	I _O =300mA V _O =V _{OUT} (E)-2%	1.3V≤V _{OUT} (T)≤2.0V	-	-	1300	mV
			2.0V<V _{OUT} (T)≤2.8V	-	-	400	
			2.8V<V _{OUT} (T)	-	-	300	
Quiescent Current	I _Q	V _{IN} = V _{OUT} (T)+1V		-	30	50	μA
Line Regulation	REG _{LINE}	I _O =1mA V _{IN} =V _{OUT} (T)+1 to V _{OUT} (T)+2	1.3V≤V _{OUT} (T)≤1.4V	-0.2	-	0.2	%
			1.4V<V _{OUT} (T)≤2.0V	-0.15	-	0.15	
			2.0V<V _{OUT} (T)<4.0V	-0.1	0.02	0.1	
			4.0V≤V _{OUT} (T)	-0.4	0.2	0.4	
Input Voltage	V _{IN}			Note3	-	7	V
Over Temperature Shutdown	OTS			-	150	-	℃
Over Temperature Hysterisis	OTH			-	30	-	℃
Output Voltage Temperature Coefficient	TC			-	30	-	ppm/℃
Short Circuit Current(Note4)	I _{SC}	V _{IN} =V _{OUT} (T)+1V V _{OUT} =0V		-	150	300	mA
Power Supply Rejection	PSRR	I _O =100mA Co=2.2μF	f=1kHz	-	50	-	dB
			f=10kHz	-	20	-	
			f=100kHz	-	15	-	
Output Voltage Noise	e _N	f=10Hz~100kHz z I _O =10mA	Co=2.2μF	-	30	-	μVrms

Note 1: V_{OUT(E)} =Effective Output Voltage (i.e. the output voltage when "V_{OUT(T)} +1.0V" is provided at the V_{IN} pin while maintaining a certain I_{OUT} value).

2: V_{OUT(T)} =Specified Output Voltage

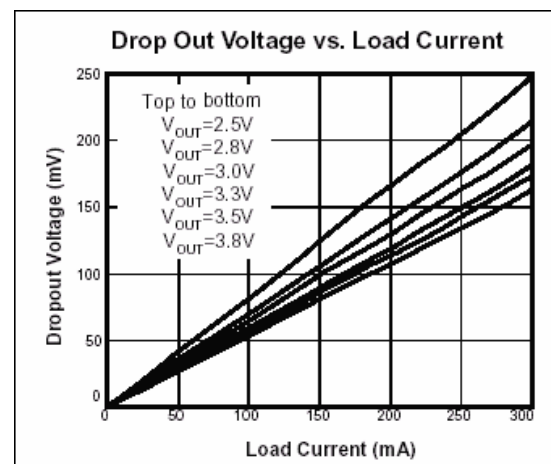
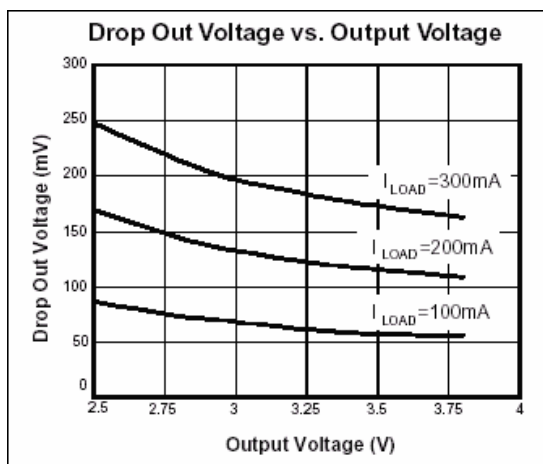
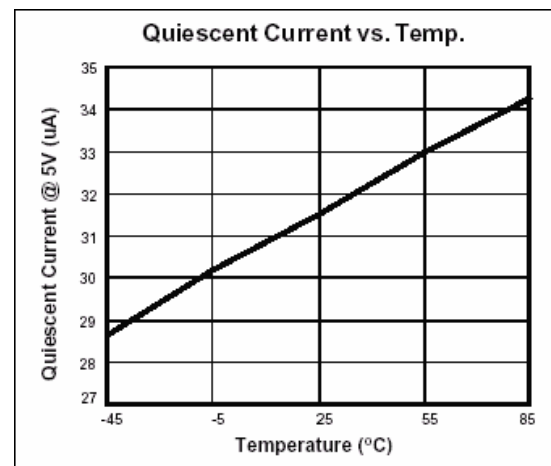
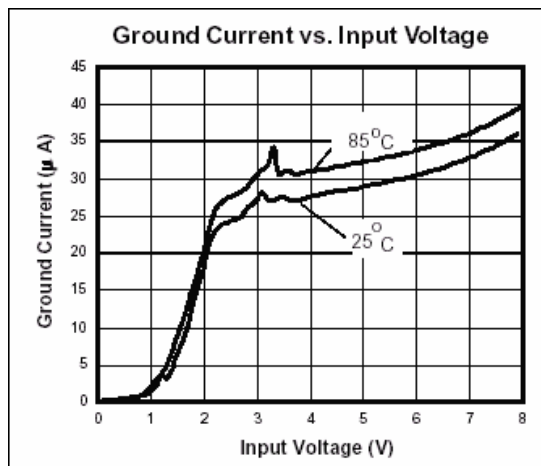
3: V_{IN(MIN)} =V_{OUT}+V_{DROPOUT}

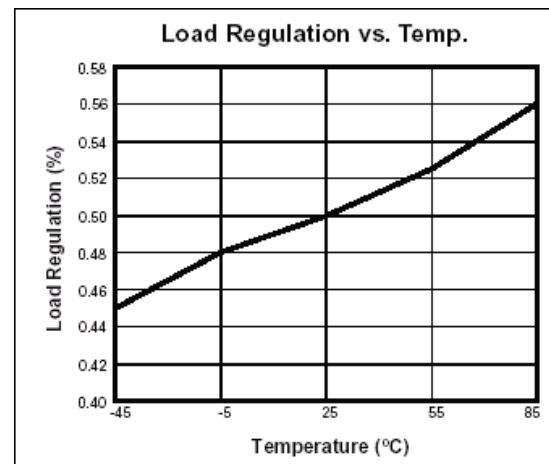
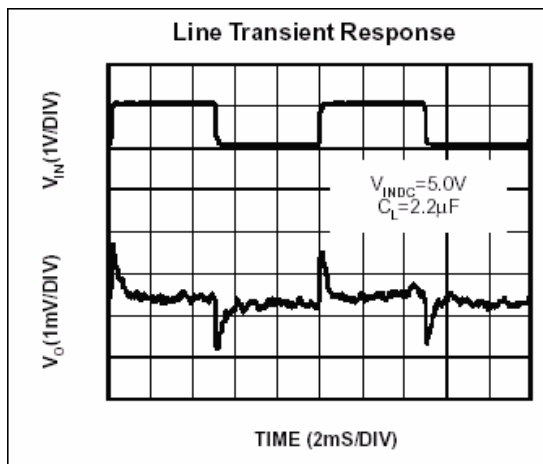
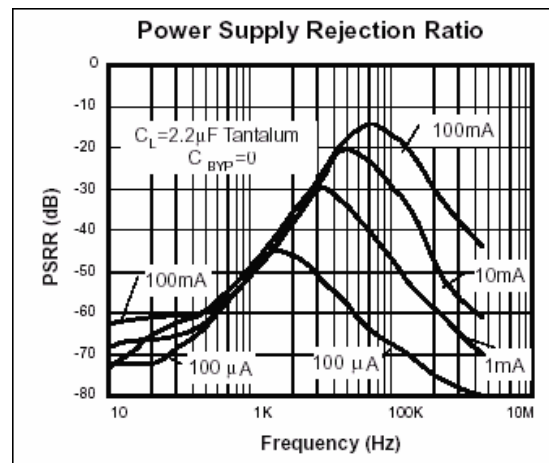
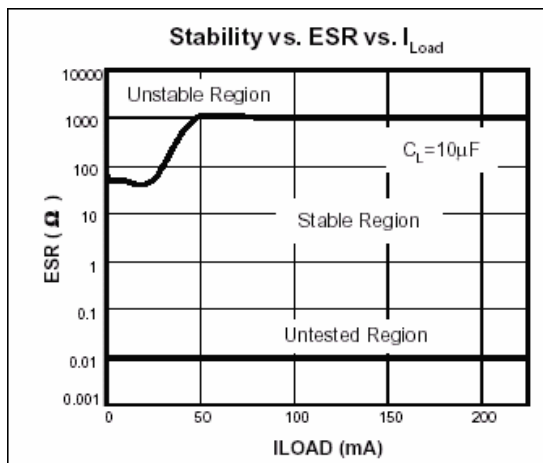
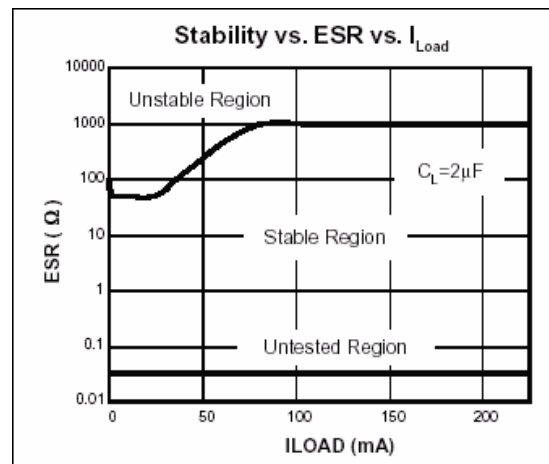
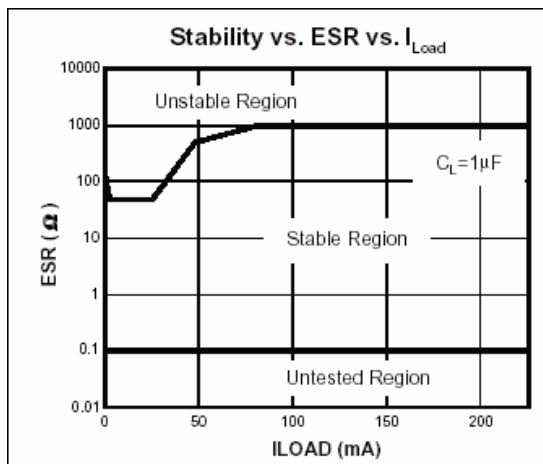
4: To prevent the Short Circuit Current protection feature from being prematurely activated, the input voltage must be applied before a current source load is applied.

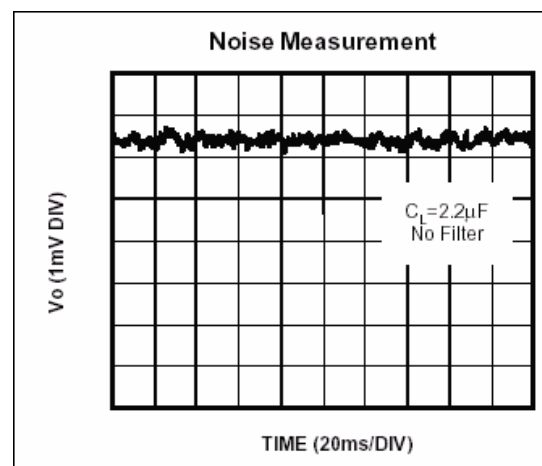
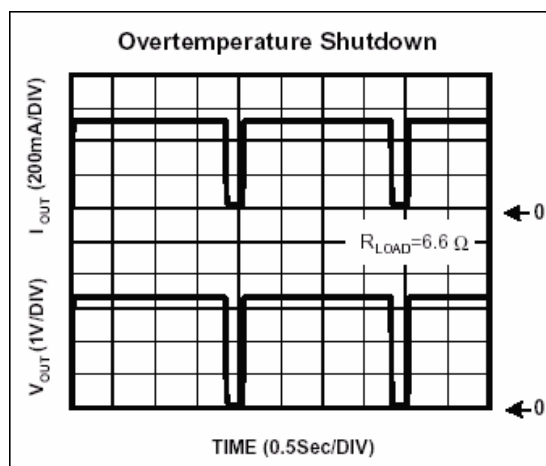
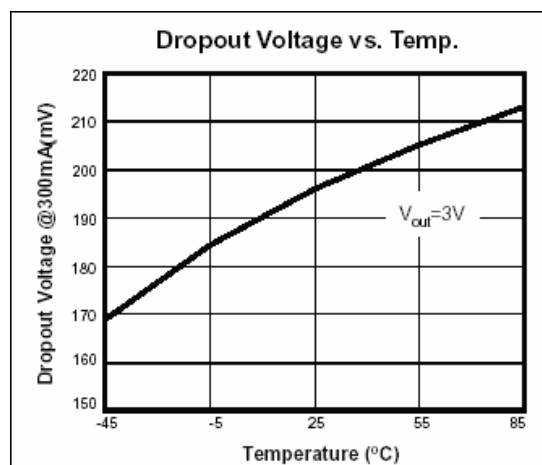
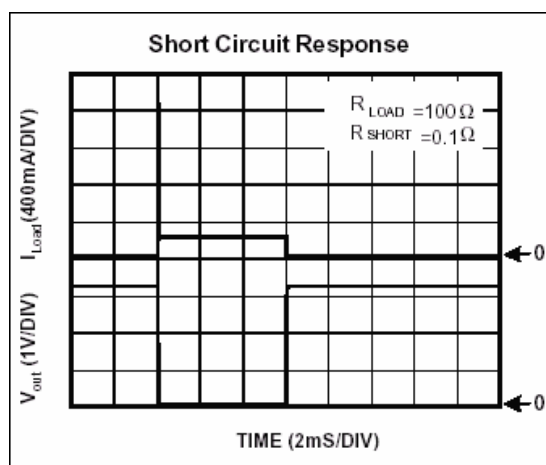
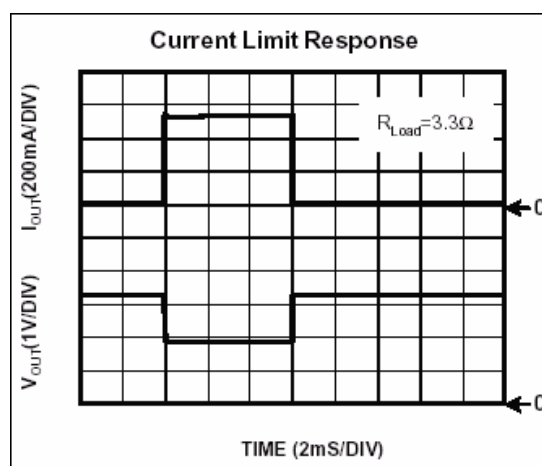
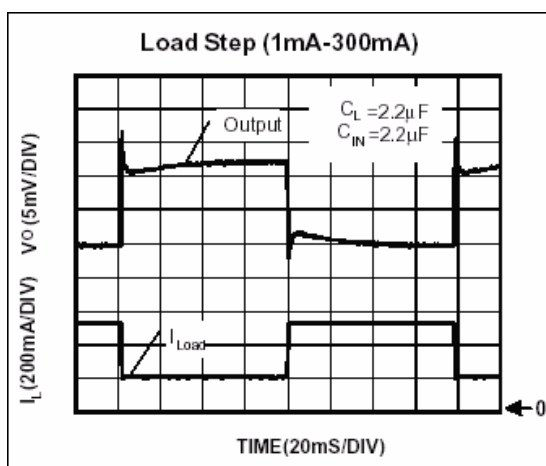
Ordering Information (contd.)

Part Number	Marking	Output Voltage	Part Number	Marking	Output Voltage
G2113-15	3P152 XX	1.5V	G2113-18	3P182 XX	1.8V
G2113-25	3P252 XX	2.5V	G2113-27	3P272 XX	2.7V
G2113-2H	3P2H2 XX	2.85V	G2113-28	3P282 XX	2.8V
G2113-29	3P292 XX	2.9V	G2113-30	3P302 XX	3.0V
G2113-31	3P312 XX	3.1V	G2113-33	3P332 XX	3.3V
G2113-34	3P342 XX	3.4V	G2113-35	3P352 XX	3.5V
G2113-36	3P362 XX	3.6V	G2113-37	3P372 XX	3.7V
G2113-38	3P382 XX	3.8V			

Characteristics Curve







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